

Development of Micro-Pores Including Nano-Pores on n-Si (100) Coated with Sparse Ag Under Dark Etching in 1.0 M NH₄F Containing 5.0 M H₂O₂

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Specimens of n-type single crystalline silicon sparsely deposited with silver nano-particles on the Si (100) surfaces were put in 1.0 M NH₄F + 5.0 M H₂O₂ to investigate their dark etching. Through examination by scanning electron microscopy (SEM), the morphology on the n-Si (100) surface etched for 1 h revealed a sparse distribution of nano-pores (10~40 nm in diameter) according to the locations of Ag-particles; however, it exhibited porous surface consisting of micro-pores (1.5~3.1 μm in diameter with 15~20 μm in depth) where nano-pores (100~150 nm in diameter) were embedded inside for the etching duration prolonged for 5 h. The Nyquist plot for this system indicated two typical semicircles, in which the one in response to high frequencies revealed greater diameter and the other in response to low frequencies indicated smaller diameter. By checking the chemical bonding of silicon and silica in the NH₄F/H₂O₂ system shows two important points at 99.3 eV and 103.4 eV.

Keywords: Porous silicon; Ammonium fluoride; Metal-assist etch; Energy band diagram

1. INTRODUCTION

The dissolution of single crystalline silicon in hydrofluoric acid (HF) was first discovered by Uhler in 1956 [1]. Turner [2], Lehmann and Gruning [3] subsequently reported on the mechanisms underlying the formation and responses of porous silicon were conducted. In 1990, Lehmann fabricated porous silicon structures with high aspect-ratios [4], whereupon applications of porous silicon expanded to microelectromechanical systems (MEMS) [5], membrane techniques [6], sensors

[7], and photonic crystals. In these areas, the etching techniques for n-type silicon require a specific anode bias voltage and light irradiation in order to form pores. In 2000, Li and Bohn developed a metal-assisted approach for chemical etching [8]. In their study, they deposited precious metals including platinum, gold, and palladium on silicon surfaces to function as metal catalysts. Wet etching in a solution of hydrofluoric acid (49 %) and hydrogen peroxide (30 %), produced a nano-porous layer without the need for applied bias voltage. In other words, at sites on silicon surface where such a suitable metal particle was deposited, etching would be strongly accelerated leading to deep channels.

Usually, hydrofluoric acid or mixtures of hydrofluoric acid/ammonium fluoride were the most common commercial pre-treatments for silicon wafers. Under different ratios of HF/NH₄F, the solutions varying in pH values were used to clean silicon wafers to remove SiO₂ layers. Oxidizer such as hydrogen peroxide was added into the etched solution to cause partial oxidation on the silicon surface. It was believed that the role of oxidizers was reacted with silicon to form silica and produce electron holes [9] which transfer to exposed silicon surface to react with fluoride ions this speeding up the etching process. Toxicity was a concern to perform this process because of high volatility of hydrofluoric acid even at room temperature. Single use of ammonium fluoride was safer than its combination with HF since NH₄F was stable to get rid of HF-volatility [10]. However, the reactivity was quite low even the concentration of NH₄F was increased up to 40% (roughly at 11.0 M) which was alkaline with the pH approximately at 8 [11, 12]. Etching studies on various crystal surfaces of silicon in 40% ammonium fluoride with the aid of anodic biases were extensively explored [13, 14]. It was seldom reported for the silicon etched in the simple NH₄F solutions without the aid of electrochemical polarization. Recently, we published a work regarding dark etching of n-Si (100) silicon coated with sparse Ag-nanoparticles in the ammonium fluoride mixed with hydrogen peroxide [15]. According to the results, we concluded that hydrogen peroxide played an oxidant not only formation of silica but also generation of electron holes on the exposed n-Si (100) surface to facilitate its etching. Under detailed examination by scanning electron microscope (SEM), we found that the n-Si (100) surface tended to develop an interesting morphology in which micro-pores were formed and a few nano-pores were embedded inside the micro-pores as the etching duration was prolonged up to 5 h. This interesting morphology in this system was seldom investigated. In the present work, we intended to focus on the occasion of this special morphology. The related kinetics and responsible mechanism for this phenomenon were also of concern. Electrochemical impedance spectroscopy (EIS) was applied in this work since it provided a powerful tool to study etching kinetics of silicon [16].

2. EXPERIMENTAL DETAILS

The substrates employed in this work were n-type (100) single crystal, dimension in 10 mm×10 mm with thickness of 525 μm, belonging to silicon wafers doped with phosphorus indicating their electrical resistivity at 4-7 Ω-cm. The specimens were sequent placed in a series of beakers containing cleaning fluids as follows: acetone, alcohol and deionized water. In conjunction with ultrasonic oscillation, the cleaning fluids removed contaminants, grease, and organic impurities. Between cleaning steps, the specimens were rinsed with deionized (DI) water followed by treating with 1 wt. %

hydrofluoric acid solution to eliminate the oxide layer from the surfaces. The specimens were dried with a purging nitrogen gas and ready for investigation.

For deposition of silver nano-particles sparsely distributed on the surface of silicon substrate, the specimens were immersed in a 150 ml bath containing silver nitrate (2.0 mM) and 0.08 M formaldehyde maintained at 25 °C for 10 minutes, then removed to proceed soft baking in an oven set at 80 °C for 1 h to evaporate the unreacted chemical reagents and moisture, thus resulted in a firm attachment of silver nano-particles sparsely distributed on the silicon surface.

The experimental set up was established in our previous work [15]. An etching tank (80 x 50 x 60 cm³) was made of poly-tetrafluoroethylene (PTFE) with an inner volume of 150 ml. A contact area of 64 mm² was opened on the wall of the tank to expose the specimen in contact with the etching solution. The Ag-deposited specimen was fixed by a sample holder made of pure copper (99.90 %). An indium-gallium alloy was filled between the specimen and copper clamp to form an Ohmic contact. A connecting lead with one end conducted to a potentiostat (Potentiostat/Galvanostat AutoLab EG&G2263) and the other connected to the sample which was acting as anode or working electrode. A piece of platinum (99.99 % pure) foil (dimension in 10 mm x 10 mm x 0.5 mm) was served as the counter electrode (i.e., cathode, or auxiliary electrode). A saturated calomel electrode (SCE) equipped with a salt bridge made of high density polyethylene (HDPE) capillary tube filled with saturated KCl solution employed as a reference electrode in this work. The potential data were reported against SCE in this work.

The system was considered to have been stable when the variation of open-circuit potential (OCP) was within ± 10 mV in the measurement period. For minimizing the possible interference caused by concentration polarization in the measuring EIS data, we stirred the solution with a magnetic stirrer to maintain solution circulation. In the performance of EIS measurement, the frequency was varied in a range from 10⁶ Hz to 0.1 Hz at 5 points/decade with amplitude of 50 mV. A number sets of theoretical equivalent circuits possibly governed the etching process in different mechanisms were proposed and they were simulated with the experimental EIS data by means of a commercial simulation software (Z-view, Schlumberger, England). After ruling out the irrational mechanisms and those responsible for the data beyond satisfactory fitting, we could choose an optimal equivalent circuit which should be responsible for this etching mechanism. Subsequent to etching, the surface of the substrate was washed with deionized water and dried. Through observation with field emission scanning electron microscope (FE-SEM, FEI-NNS230), we could examine the morphology on the top surface and cross-section of the specimens in detail. The equipped energy-dispersive spectrometer (EDS, Bruker Quantax 200) was used for elemental analysis. The chemical state on the silicon surface was analyzed by X-ray photoelectron spectroscopy (XPS PHI 1600).

3. RESULTS

3.1. Surface morphology of porous Si depended upon the etching duration.

Figure 1 depicted the top views of SEM for n-Si (100) post wet etching for 1 h (Figs. 1 a and c) and 5h (Figs. 1 b and d) in dark 1.0 M NH₄F + 5.0 M H₂O₂ aqueous solutions. Figure 1 (c) revealed the

magnified micrograph for the location circled in Fig. 1 (a). It was seen that a number of nano-pores were formed below the locations where Ag nano-particles deposited after performance of 1h-etching. With prolonging the etching duration to 5 h, the nano-pores developed to form micro-pores (diameter in the range from 1.5~3.1 μm), as shown in Fig. 1 b. Further detailed the morphology at the circled area marked in the inset of Fig. 1 b, as indicated in Fig 1 d, we found a number of nano-pores embed inside the micro-pores.

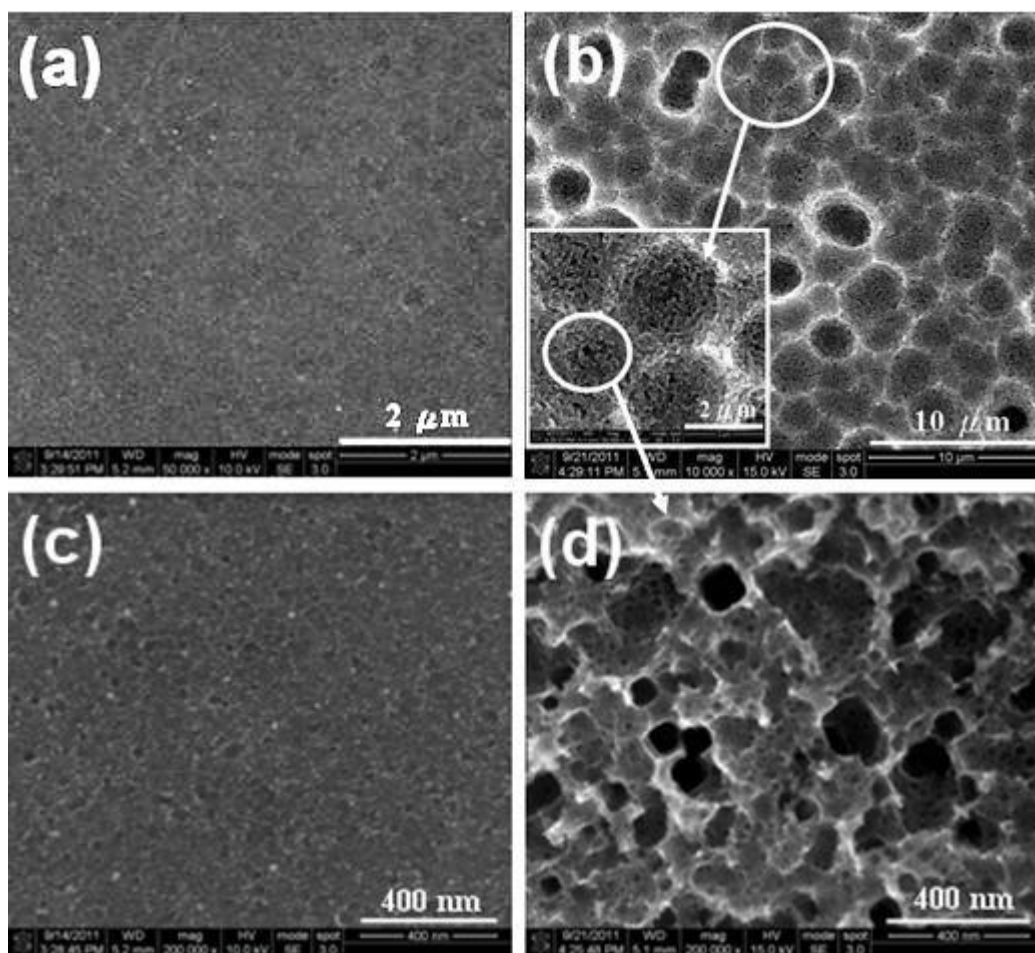


Figure 1. SE morphologies on the n-Si (100) coated with sparse Ag nanoparticles post dark etching in an aqueous solution of 1.0 M NH_4F + 5.0 M H_2O_2 for (a) 1 h and (b) 5 h in lower magnification (X 50 k); also for (c) 1 h and (d) 5 h but in higher higher magnification (X200 k).

Figure 2 demonstrated the cross-sectional SEM morphologies on the n-Si (100) corresponding to Fig. 1 variant with etching duration in 1 h and in 5 h, respectively. As depicted in Fig. 2 a and its higher magnification shown in Fig. 2 c, numbers of nano-pores were formed on the top Si (100) surface with an average depth at 50 nm and few of them even with greater depth down to 200 nm when the etching time was stop in 1 h. The absence of silver nanoparticles may be resulted from cleaning after sliced apart the specimen prior to the examination by SEM. It was worth noting that almost nano-pore revealed a smooth profile that implied that walls of them might be free from sub-pores inside. In

contrast, the average depth for the micro-pores resulted from prolonging etching (in 5 h) was rough at $15\mu\text{m}$ and some of them even with depth greater than $20\mu\text{m}$, as shown in Figs. 2 b and d. The discrete pores far below the porous surface might derive from smaller sidewall pores originated from the adjacent or next vertical pores. This fact reflected the smaller pores embedded on the sidewall and bottom of the micro-pores. The absence of Ag particles might also be ascribed to the cleaning effect of the specimens prior to observation by SEM. Evidently, the diameter and depth of the pores increased with increasing the etching duration from 1 to 5 h for this system.

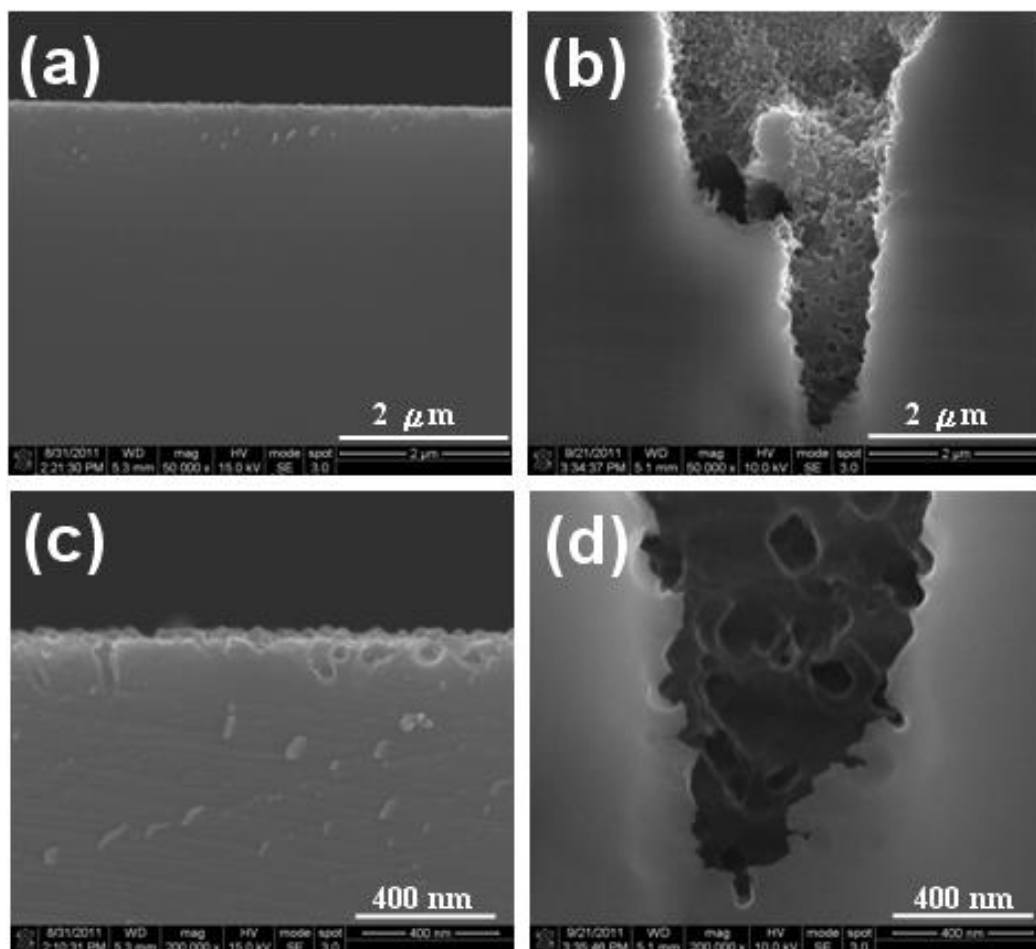


Figure 2. Cross-sectional SE morphologies for n-Si (100) coated with sparse Ag nanoparticles post dark etching in 1.0 M NH_4F + 5.0 M H_2O_2 solution for (a) 1 h and (b) 5 h at lower magnifications (X50 k); also for (c) 1 h and (d) 5 h in higher magnifications (X200 k).

3.2. XPS analysis of the films

Prior to dark etching, analysis of EDS on the sparse Ag-coating Si (100) revealed only two elements such as silver and silicon are present. After etching in the solution for 1 h and 5 h, not only the presence of silver and silicon but also oxygen was found at the concentration of 0.55 wt% and 0.73 wt%, respectively. This fact implied oxides formation on the specimen post dark etching. Figures 3 (a) and (b) displayed XPS spectra on the etched surface of the specimen etched for 1 h and 5 h in the dark

solution of 1M NH_4F contain 5.0 M H_2O_2 . Referring to handbook and literature, we identified the two main peaks as silicon (Si) and silica (SiO_2) with their binding energy at 99.3 eV [17] and 103.4 eV [18], respectively. It was found that a few silicon oxides such as Si_2O and SiO and Si_2O_3 in dilute but various concentrations, resultant from the curve valley between the two main peaks belonging to Si and SiO_2 , were involved in Fig. 3 (a). A similar result was observed but almost lack of Si_2O_3 in Fig. 3 (b). These results reflected the oxidation of silicon undergoes a gradual transition from 0, +1,+2, +3 unstable valence states to reach the most stable +4 state. The peak intensity is proportional to the concentration of the oxides. Less intensity is the peak indicates the less stability of the oxide. The oxide with +1 (i.e., Si_2O) valence state is the most stable species among the intermediates. Prolonging the etching duration from 1 h to 5 h, the most unstable +3 state almost disappeared. The oxidation of silicon led to not only dissolution but also formation of silicon oxide on the specimen surface.

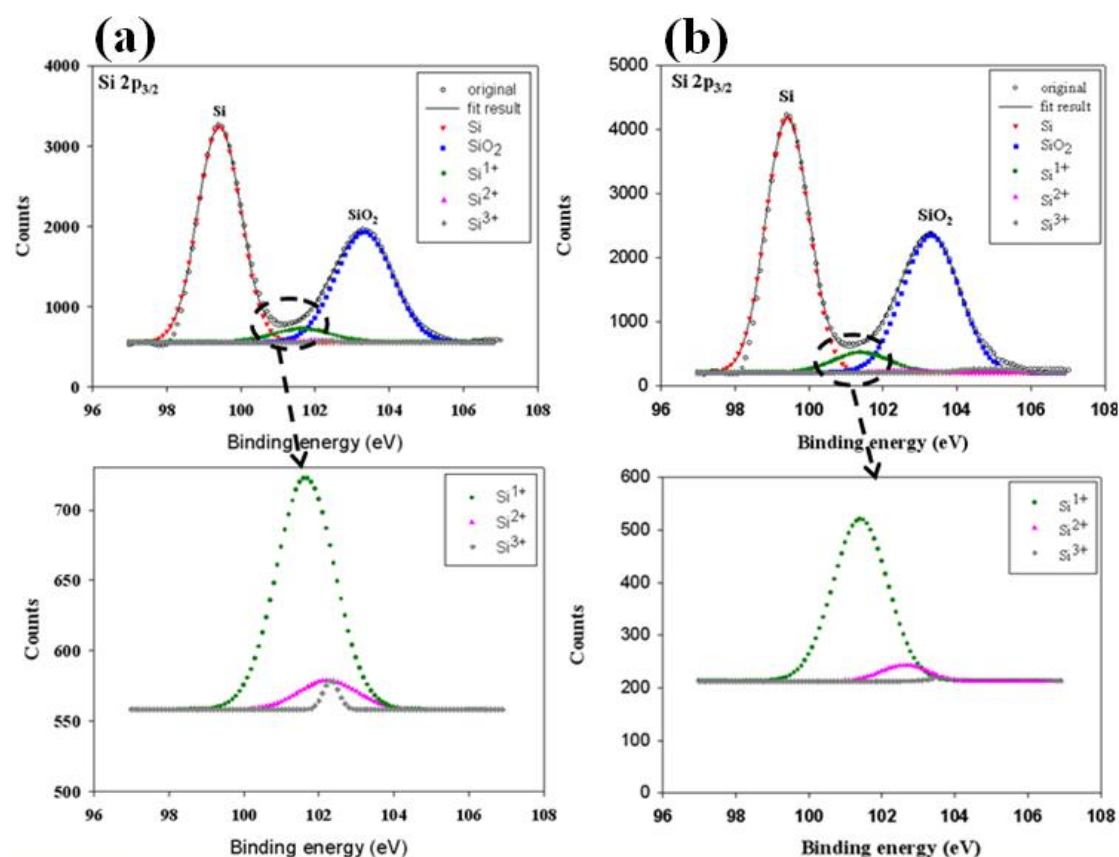


Figure 3. XPS for Si (2p) of the specimen etched in dark solution of 1.0 M NH_4F containing 5.0 M H_2O_2 post (a) 1 h and (b) 5 h, respectively.

3.3. EIS of *n*-Si (100) at open circuit potential in 1.0 M NH_4F + 5.0 M H_2O_2

The information resulted from EIS study was useful to comprehend the characteristic of an interface, including the structure of electrons in the interfacial layer, charge storage (the space charge region). These properties could be delineated on EIS measurements by changing the frequency from 10^6 to 10^{-1} Hz. Figure 4 revealed the Nyquist plot for the etching system in 1.0 M NH_4F + 5.0 M H_2O_2

solution. In Fig. 4, there existed two distinct capacitance arcs which might be ascribed to etching dissolution and the oxidation of silicon on the interface. At first glance of this plot, the magnitude of both the arcs increased with increasing the etching duration from 1 h to 5 h. The increment of the first one seemed to be greater than that of the second.

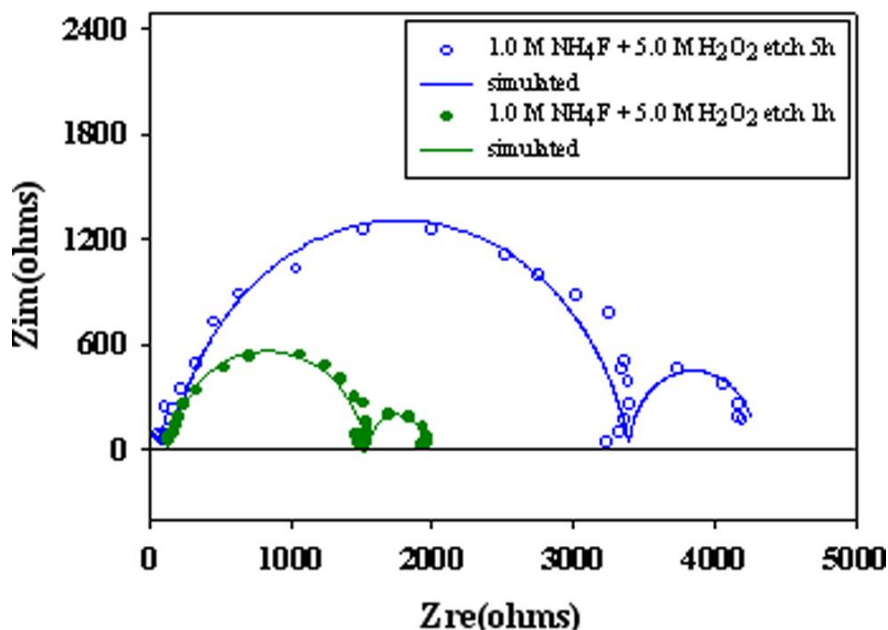


Figure 4. Nyquist plots for the wet etching of n-type silicon in the dark of 1.0 M NH_4F + 5.0 M H_2O_2 with 1h and 5h, respectively.

4. DISCUSSION

4.1. Analysis of EIS data and equivalent circuit proposed

The impedance may comprise the components such as solution resistance, charge-transfer resistance, mass-transfer resistance, capacitance of the electrical double layer and capacitance of the space charges in the semiconductor in terms of their possible combination in parallel or in series [19]. Using the commercial software Z-view to simulate the experimental data of impedance measurements with possible equivalent circuits proposed on basis of rational combinations of those aforementioned impedance components, we could find out the best fit that is meaningful theoretically. Eventually, one most satisfactory set of equivalent circuit was chosen from all the possible sets to stand for the electrochemical kinetics. This equivalent circuit was depicted in Fig. 5. In Fig. 5, the equivalent circuit consisted of solution resistance R_1 , which combined in series with a subcircuit resultant from electric double layer at the interface, including the resistance (R_2) and consists of a pseudo-capacitor element (CPE_2) [20]. The resistance of oxide layer combined in series with another subcircuit consisting resistance (R_3) and capacitance (C_3) those were arisen from the silica. In the dark etching of the n-Si (100) coated with sparse Ag-particles in 1.0 M NH_4F + 5.0 M H_2O_2 , the solution was kept on stirring

by a magnetic stirrer as described in the experimental section. No presence of any diffusion element in the proposed equivalent circuit may be ascribed to effective stirring in the solution. The data resulted from proposed equivalent circuit as indicated in Fig. 5 were summarized in Table 1.

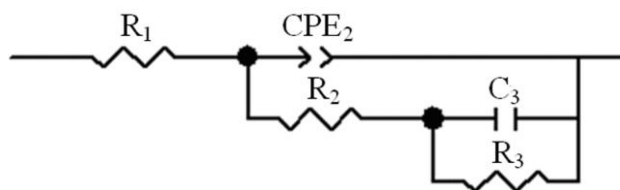


Figure 5. Proposed equivalent circuit responsible for the kinetics and mechanism in the dark etched of n-Si (100) in 1.0 M NH₄F + 5.0 M H₂O₂ solution.

Table 1. Theoretical data based on the proposed equivalent circuit which revealed the most satisfactory fitting to the EIS experimental measurements in the dark etching of Si (100) coated sparse Ag nanoparticles in 1.0 M NH₄F + 5.0 M H₂O₂ in different durations.

Etching duration	Data of the elements						
Symbols of elements (unit)	OCP (mV)	R1 (Ω)	CPE2-T (F)	CPE2-P	R2 (Ω)	C3 (F)	R3 (Ω)
Etching for 1h	-367	125	6.7E-07	0.85	1430	0.01	400
Etching for 5h	-386	150	3E-07	0.86	3250	0.004	900

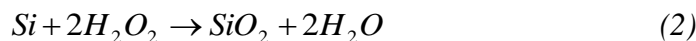
3.2. Development of the reaction models for this etching system

As the specimen was immersed in the etching solution, the silicon surface free from Ag-coating was exposed to react with hydrogen peroxide and Ag-particles would catalyze the reduction of hydrogen peroxide to undergo silicon etching as described in our previous work [15]. Indirectly evident from Figs. 3 (a) and (b), we suppose that the free surface on n-Si (100) tends to form a stable film of silicon oxide (SiO₂) involving a sequential steps via formation of few metastable intermediates such as Si₂O (oxide of Si⁺), SiO (oxide of Si²⁺) and Si₂O₃ (oxide of Si³⁺) in the following.

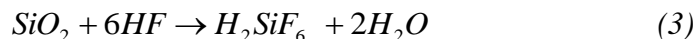


Resultant from the concentration of oxides shown in Fig. 3, we infer that tiny concentration of the intermediates Si₂O₃ and SiO in contrast to Si₂O is ascribed to their high instability. The step

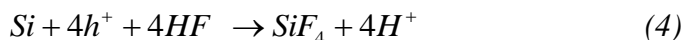
involving formation of somewhat stable Si_2O could be considered as the rate determining step in the overall reaction



This result is consistent with that in the literature [21-24]. This silicon oxide could be dissolved in fluoride solutions to various extents depending upon fluoride concentrations [25] by obeying the equation

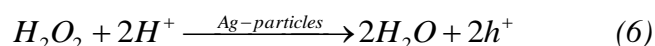


This dissolution facilitates the exposure of silicon to contact with silver particles detached from other sites, thus undergoing another dark etching. In other words, direct dissolution of silicon occasions involving the participation of electron holes as follows



According to equation (4), the participation of electron holes leads to facilitate silicon dissolution. Occurrence of SiF_4 in equation (4) tends to react with fluoride ions to form H_2SiF_6 that is soluble in the aqueous solution.

On the other hand, on the silicon surface coated by Ag-particles, hydrogen peroxide is catalytically reduced on the silver particles to create electron holes in the following.



These electron holes pass through the silver particles to reach Si-substrate thus creating anodic sites to undergo dissolution (via equations (4) and (5)) hence resulted in local etching on silicon [26]. It is obvious that all the equations from equation (2) to equation (6) summarized the electrochemical reactions resulted in dark etching of silicon in ammonium fluoride solution. Dark dissolution of silicon in ammonium fluoride via equations (2) and (3) is facilitated by dissolution of silicon oxide; however, silicon undergoes direct dark dissolution via equations (4), (5) and (6) is facilitated by holes catalytically created in the presence of Ag-particle coating on the silicon.

Based on the mechanism described, we are capable of using a schematic model, as depicted in Fig. 6, to illustrate the dark etching of silicon. In the semi-conductor like n-Si (100), electrons and electron holes are separately distributed to establish a space-charge region (SCR). The thickness of SCR in contrast to wall thickness of the pores in silicon plays an important role on the etching morphology. In the case where the thickness of SCR is greater than that of porous walls, the holes are

inclined to concentrate on the tips of the pores thus resulting in etching downwards to form vertical pores with smooth walls, as indicated in Fig. 2 (c), in the absence of side etching in the duration of 1 h.

Prolonging the etching duration to 5 h, the attack on the sidewalls were inevitable, as depicted in Fig. 2 (d). Hence, nano-pores were embedded inside the micro-pores. The occasion of nano-etching on the sidewall is believed due to thickness shrinkage of the SCRs in contrast to the walls of micro-pores. It is generally accepted that the Fermi level potential (E_F) of n-Si (100) is higher than the redox potential (E_{redox}) of the solution prior to the contact between them. As soon as they contact together, the electrons tend to flow from the higher Fermi level of n-Si (100) down to the lower redox potential level of the solution and eventually they reach the equilibrium state with identical potential (i.e., $E_F = E_{\text{redox}}$) [27]. The flow of electrons leads to create excessive charges (i.e. positively charged holes) that capable of separation of those different charges distributed on the end-surfaces in the regions of the silicon which are known as the space charged regions (SCRs). The electric field induced by the SCRs leads to a downward bending of the conduction band of silicon (i.e. E_C) against the conduction band at the interface (i.e., E_{CS}) and so does the valence band of silicon (i.e., E_V) against the valence band at the interface (i.e., E_{VS}) in the same magnitude of potential shift from E_F to E_{redox} . In contrast to the data of CPE₂-T in Table 1, the capacitance is 6.7×10^{-7} F for 1 h-etching and is 3.0×10^{-7} F for 5 h-etching. A diminishment of rough 55 % in the capacitance is resultant from prolonging the duration from 1 h to 5 h.

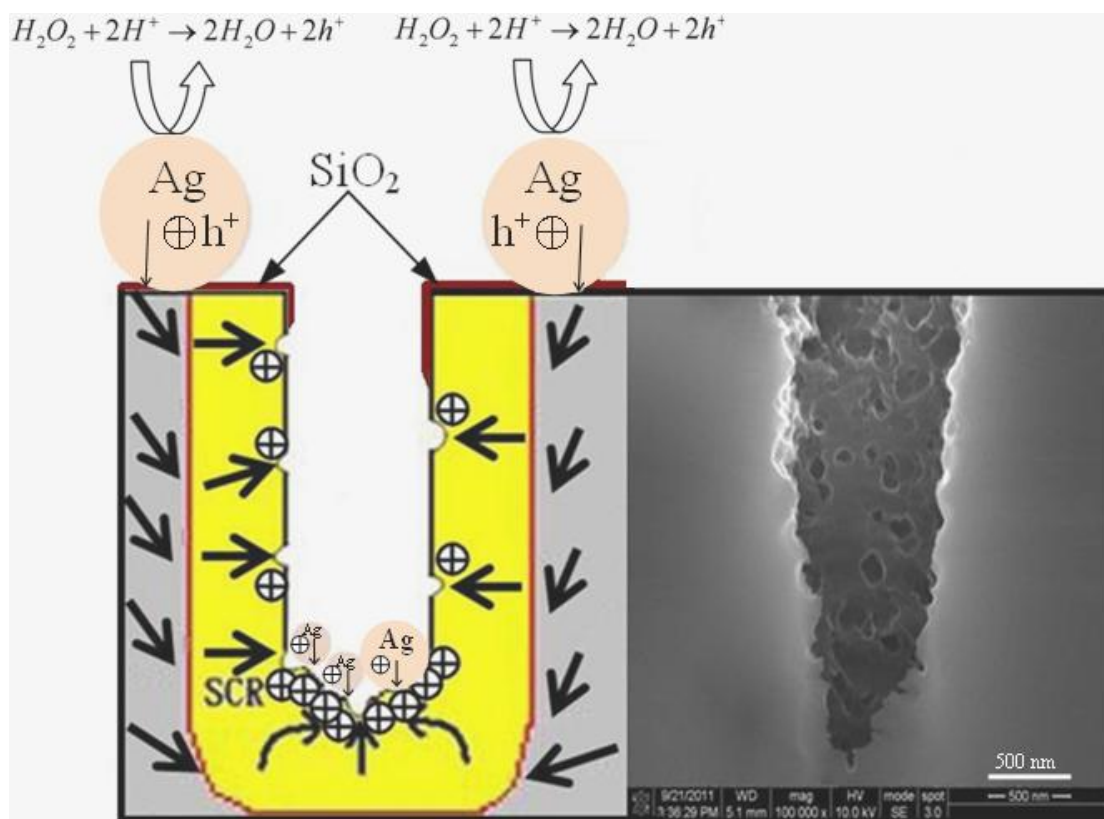


Figure 6. Schematic models for the dark etching of n-Si(100) coated with sparse Ag nano-particles performed in 1.0 M NH₄F + 5.0 M H₂O₂ for 5 h. The arrows indicate the motion direction of the electron holes; SCR revealed the space-charge regions.

This decrease in capacitance reflects less density of the charges distributed in the electric double layers, so that the electric field exerted in the SCRs is reduced, and the bending of the energy bands is significantly mitigated. Due to this fact, the positively charged holes tend to drift much readily in the sidewalls of the micro-pores whose thickness of SCRs is thinner. The probability of holes appearing at the sidewalls with thinner SCRs (at the duration of 5 h) is much greater than those with thicker SCRs (at the duration of 1 h). Consequently, the formation of nano-pores on the sidewalls of micro-pores with longer the duration up to 5 h could be realized. Accordingly, we conclude that micro-pores with thick walls provide sufficient pathways of the holes to pass through from the Ag-coating top to the sites under various depths. On the other hand, plentiful holes are created and accumulated at the pore tip because of catalytic reduction of hydrogen peroxide with Ag-particles. The extensive attack of plentiful holes results in micro-pores; in comparison, few holes distributed on the sidewall confine to form nano-pores. Consequently, dark etching of this system gives rise to formation of micro-pores (in an average diameter of 1.5~3.1 μm and depth of 15~20 μm .) embedded with nano-pores (100~150 nm) under prolonging the duration up to 5 h.

3.3. Confirmation of the etching kinetics

Figure 7 exhibited the plots of phase angle against logarithmic frequency for this system varying in etching durations. According to plots in Fig. 7, we found only two time-constant spectra centered at roughly $10^3\sim 10^5$ Hz and 10^{-1} Hz, respectively. These two spectra implied the possible formation of an oxide layer on the specimen surface [28] correlated to the kinetics occasioned at the interface. In contrast to the two curves, we found that prolonging the duration tends to increase the phase angle from 45 to 60 degree at higher frequency peak; however, no significant change in the phase angle at the lower frequency.

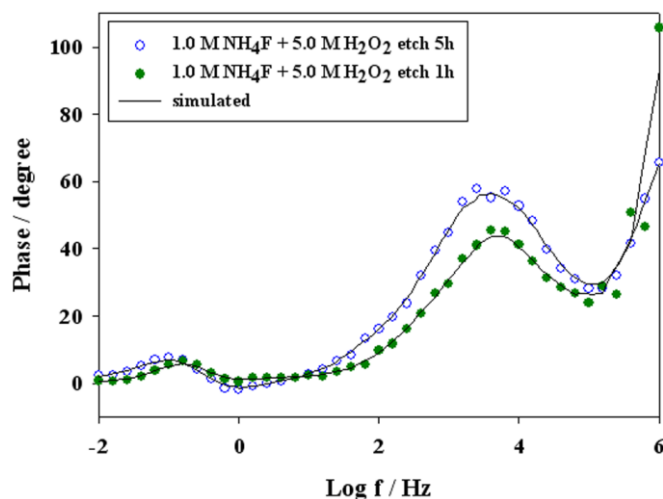


Figure 7. Plots of phase angle against logarithmic frequency used in EIS study for the dark etching of n-Si (100) coated with sparse Ag nanoparticles in the solutions of 1.0 M NH_4F + 5.0 M H_2O_2 for 1 h and 5 h.

The evidence of oxide formation was in agreement with Niwano *et al.* [29]. They concluded that the thickness of the oxide layers depended on the concentration of ammonium fluoride. Under the concentration less than 10% (i.e., 2.8 M), the oxide layer was established to have a thickness that was inversely proportional to the concentration of ammonium fluoride.

4. CONCLUSIONS

Dark etching of n-Si (100) coated with sparse Ag nano-particles in 1.0 M NH_4F + 5.0 M H_2O_2 resulted in distinct morphologies depending upon the etching duration. The silicon surface revealed a sparse distribution of nano-pores (10~40 nm in diameter) resultant from the coating Ag-spots within 1 h-etching; the surface turned into a distribution of micro-pores (1.5~3.1 μm in diameter with 15~20 μm in depth) in which nano-pores (100~150 nm in diameter) embedded inside for prolonging the etching up to 5 h.

EIS study was useful to explore the electrochemical kinetics and delineate the mechanism. The Nyquist plot depicted two typical semicircles in which the one centered at higher frequencies was greater than that centered at lower frequencies. The greater semicircle grew faster than the small one with increasing the etching duration. An equivalent circuit was proposed through simulation with commercial software by selecting the best fit of theoretical sets with the experimental data, we successfully constructed a schematic model to describe the dependence of etching morphology on the duration.

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